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Applicant: Mansoori et al.

Art Unit: 2812

Serial No.: 10/620,492

Examiner: Lindsay, Jr., W.

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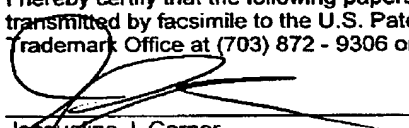
Title: METHOD TO REDUCE TRANSISTOR GATE TO SOURCE/DRAIN OVERLAP
CAPACITANCE BY INCORPORATION OF CARBON

AMENDMENT UNDER 37 CFR 1.111

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being
transmitted by facsimile to the U.S. Patent and
Trademark Office at (703) 872 - 9306 on 5/31/2005.


Jacqueline J. Garner
Reg. No. 36,144

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated 12/01/2004. They are respectfully submitted as a full and complete response to that Action.

Please amend the above-referenced application as follows:

The **Amendments to the Specification** begin on page 2.

Amendments to the Claims are reflected in the listing of claims that begin on page 3 of this paper.

The **Amendments to the Drawings** begin on page 7.

The **Remarks** begin on page 8.